

VAMSI BOLLA



Personal info:

- E-mail:
bollavamsi777@gmail.com
- Phone : 9381356291
- DOB: 21ST Jan 1999

Technical Skills:

- C
- Embedded C
- Linux OS
- SDK's & Development Kits
- Spectrum Analyzer
- Oscilloscope
- Logic Analyzer
- JTAG

Languages:

- English
- Telugu

Marital status:

- Unmarried

➤ CAREER OBJECTIVE:

To obtain a challenging role as an Embedded Systems Engineer, where I can apply my 2 years of experience in firmware development, and hardware interfacing to contribute to innovative embedded solutions.

➤ EDUCATION:

2018-2022

- **Bachelor of Technology**
(Electronics&CommunicationEngineering)
Newton's institute of science and Technology
(JNTU-KAKINADA)
Alugurajapalli (village), koppunur(post), Macherla,
522426,
 - **62%**

2016-2018

- **Intermediate**
MSR junior college, valaparla, bapatla, Dist,523260, AP
 - **64%**

2015-2016

- **SSC**
z.p high school, valaparla, bapatla, Dist, 523260, AP.
 - **80%**

➤ ACADEMIC PROJECT:

Title:

AUTOMATIC DETECTION OF POTHOLES AND HUMPS ON THE ROADS

Technologies: Ultrasonic Sensor, GPS, GSM Module and Arduino

Duration: 2 Months

Now a days most of deaths occurring due to accidents due to low maintenance of roads. To avoid this problem, we have implemented the project. In this project we had used ULTRASONIC SENSOR, GSM Module, GPS are the major components.by using sensor, we transmit sound waves. It detects the potholes and humps and alert the driver and as well as send the particular location to the higher authorities to rectify the problem.

➤ **TRAINING:**

Address & other details:

Embedded course in **VECTOR INDIA** institute Bangalore.

Duration: Aug-2022 to 29-May-2023.

Protocols:

- **UART**
- **I2C**
- **SPI**
- **CAN**
- **C & Embedded C**

➤ **WORK EXPERIENCE:**

Junior Embedded Software Engineer

Elementure Pvt Ltd Bangalore

- Duration July-2023 to July-2024
- At starting they have involved me in soldering the PCB boards with packages of 0603 and 0402 and came to know about studying the data sheet and came to know about pin configurations of different IC's.
- After that I have been involved in their main project "WATER METER" there I came to know practical knowledge on SPI protocol
- In linux environment I have tested around 200 devices over connectivity, data missing, range, and their behavior over traffic
- Testing experience of micro controllers over L3(IP) ,gateway and flashing code , Booting OS
- Worked on LED,ADC,SW,555 TIMER
- I have working knowledge on Zephyr SDK along with Nordic development kit (nrf52840 dk)

Embedded Engineer-1

Neelgreev Systems Hyderabad

- Duration Aug-2024 to Present.
- Working Knowledge on interface development between FPGA and RF with JESD(SERDES interface) and device drivers for real-time control RF cards,ADCs,DACs in 5G system via SPI and I2C protocols.
- Worked on mmwave RF systems involving Texas Instruments(AFE8030), Sivers EVK and Intel(FPGA) Boards
- Utilized R&S Spectrum Analyzer for analyzing signal integrity and debugging clock offset issues around 28 GHz
- Responsible for modifications and maintenance of Device Drivers for 5G Physical layer.

- Experienced with tools like TICS Pro for handling clock and AFE80xx GUI for TI chip setup.

➤ **WORKSHOPS ATTENDED:**

- Designed AC to DC converter
- **Venue:** Newton institute of science and technology
- **Duration:** from 16-18 Aug 2020

➤ **DECLARATION:**

All the information and details provided in above is true and genuine to the best of knowledge.

Place: Hyderabad

Date:

Signature

Vamsi.B